

DDCs and DUCs for Wireless Communications

1998 ISART SYMPOSIUM

Biography for Lars Jorgensen

Lars Jorgensen received a BSEE degree from San Jose State University in 1978 and an MSEE degree from Stanford University in 1982. While at Stanford he took one of the first Mead/Conway VLSI courses and used it as a basis for establishing a VLSI design center at his employer's facility (ESL). His work at ESL included developing an industry leading FFT chip set, and developing a new business area in processing digital telecommunications signals using structured custom CMOS. In 1992 he joined Graychip as the second employee where he is responsible for developing dedicated function signal processing chips. These include a variety of digital receivers from a multi-mode halfduplex 1MSPS transceiver for a flexible military HF radio to a 500 MSPS digital down converter for radar applications. His work currently focuses on cellular telephone basestation applications of digital down converters and digital up converters.