

Brief Biography

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Dr. Chris Dick joined Xilinx Inc. in January 1998 where he is a Senior Systems Engineer in the *Intellectual Property Development Group*. From 1985 to December 1997 he was a professor in the *Department of Electronic Engineering, La Trobe University*, Melbourne Australia. Chris taught courses in the area of signal processing, computer architecture and reconfigurable computing. In addition to his University work, Chris operated a signal processing consultancy that provided DSP services to the private sector.

Chris' research interests are in the areas of fast algorithms for signal processing, hardware architectures for real-time signal processing, parallel computing, interconnection networks for parallel processors, and the use of Field Programmable Arrays (FPGAs) for custom computing machines and signal processing. Chris has published many papers in the fields of signal processing, parallel computing, Inverse Synthetic Array Radar (ISAR) imaging, and the use of FPGAs for building computing platforms.